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| FORM PTO-1449 | Atty. Docket No.: L13.12-0157/01-191 | Appl. No.: 09/879,416 |
|  LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT | First Named Inventor: | |
| | Ian M. Flanagan et al. | |
| | Filing Date | Group Art: |
| | June 12, 2001 | |

U.S. PATENT DOCUMENTS

| Examiner Initial | Document No. | Date | Name | Class | Sub Class | Filing Date If Appropriate |
|------------------|--------------|------|------|-------|-----------|----------------------------|
| AA | | | | | | |
| AB | | | | | | |
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FOREIGN PATENT DOCUMENTS

| | Document No. | Date | Country | Class | Sub Class | Translation Yes No |
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| AL | | | | | | |
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OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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|-------------------------|----|--|
| <i>pk</i> | AO | F.M. Gardner, "Charge-Pump Phase-Lock Loops", IEEE Transactions on Communications, Vol COM-28, No. 11, November 1980, pp. 1849-58. |
| <i>pk</i> | AP | W.F. Egan, "Phase Lock Basics", Wiley Interscience Publications, New York, 1998, pp. 90-91. |
| <i>pk</i> | AQ | P.V. Brennan, "Phase Locked Loops: Principles and Practice", McGraw-Hill, New York, 1996, p. 35. |
| <i>pk</i> | AR | D.H. Wolaver, "Phase-Locked Loop Circuit Design", PTR Prentice Hall, New Jersey, 1991, pp. ix-x. |
| EXAMINER: <i>pk whh</i> | | DATE CONSIDERED: <i>8/12/05</i> |

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.